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NO. 8122 P. 5

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Yeshwanth Narendar et al.

Title: METHOD FOR TREATING SEMICONDUCTOR PROCESSING
COMPONENTS AND COMPONENTS FORMED THEREBY

App. No.: 10/824,329

Filed: April 14, 2004

Examiner: Julio J. Maldonado

Group Art Unit: 2823

Customer No.: 34456

Confirmation No.: 5396

Atty. Dkt. No.: 1075-E4371

MS AMENDMENT
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action mailed February 3, 2006, please amend the above-identified application as follows:

CERTIFICATE OF TRANSMISSION/MAILING	
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to the Commissioner for Patents on <u>07/13/06</u>	
<u>Elise K. Dougherty</u>	<u>E.K. Dougherty</u>
Typed or Printed Name	Signature